

Patent Abstracts of Japan

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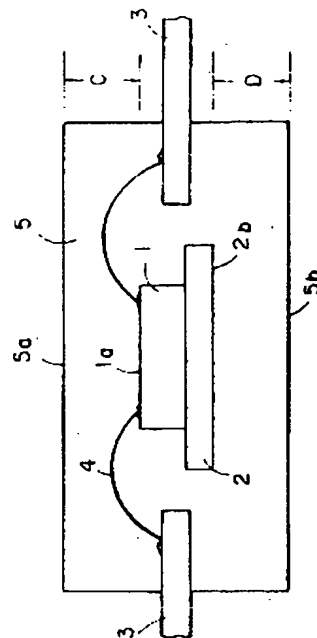
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APPLICANT : NEW JAPAN RADIO CO LTD;

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TITLE : MANUFACTURE OF  
SEMICONDUCTOR DEVICE



ABSTRACT : PURPOSE: To reduce the stress exerted for a semiconductor element by a method wherein an integrated semiconductor element and a diastand are located on almost central position in the thickness direction of a molding resin.  
CONSTITUTION: A lead 3, an integrated semiconductor element 1 and a diastand 2 are located on almost central position in the thickness direction on a resin 5. Then the element 1 and the diastand 2 are resin-sealed so that C may be equalized to D (i.e. C=D) assuming the gap between the upper surface 1a of the element 1 and the upper surface 5a of a resin 5 to be C likewise the gap between the lower surface 2b of the diastand 2 and the lower surface 5b of the resin 5 to be D. Resultantly the stress exerted for the element 1 may be reduced preventing its characteristics from deteriorating since the upper and lower thickness from the element 1 and the diastand 2 are almost equivalent to each other so that the upper and lower stress in case the resin 5 happens to contract may be almost equivalent to each other.

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